Resource Contention Analysis in GPU-Accelerated Embedded Platforms

Hipert/Lab

High Performance Real Time Lab

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Filippo

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Hipert/Lab

High Performance Real Time Lab

- Autonomous Vehicles







Filippo

- Post-Doc @ UNIMORE



High Performance Real Time Lab

- Embedded Systems

Accelerators





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Small Size

- More suitable to be placed

Small Weight

- Less load for vehicle

Small Power Consumption

- Less power demand from battery



Small Power Consumption

- Less power demand from battery
- Less computational power
- Longest algorithms execution time



Small Power Consumption

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Exploit GPU to reduce the execution time

- Localization
- Planning







Execution time is a constraint but it depends on:

- System State
- Environment State

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The ORB-ex execution time depends on the amount of extracted points

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More points \rightarrow More memory accesses and More computation

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Some processes can start in unpredictable way.

They impact the resource contention

GPU can be used to reduce the computational time:

- Parallelization of algorithms
 - Less execution time
 - Scalability
- Concurrent execution
 - CPU can perform other tasks

But the use of the GPU increases the resource contention

But the use of the GPU increases the resource contention:

- GPU is itself a resource of the system
- Memory
- Caches
- Streaming Multiprocessors (SMs)

- GPU as resource
- Memory
- Caches
- SMs



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LLC (L2) interference experiments

- One task (vadd or gemm) on 1 SM
- Interference task on another SM

Interference task access memory with a stride to dirty the caches

	Thread_0	Thread_1	Thread_2	Thread_3	
	-	ļ			
Line 0	0	1	2	3	 31
Line 1	32	33			
Line 16383					

LLC (L2) Interference results



Slowdown caused by interference on L2 cache (Kernel)

LLC (L2) Interference results



Slowdown caused by interference on L2 cache (Copy Engine)

SMs Contentions experiments

- One task (vadd or gemm) on 1 SM
- Interference tasks on other SMs

Varying the number of interferences tasks

SMs Contentions results



Slowdown caused by interference on L2 cache + SMs contentions (Jetson Orin)

SMs Contentions results



Slowdown caused by interference on L2 cache + SMs contentions (Jetson Xavier)

Global Memory Interference experiments

- Path planner that runs on GPU
- Interference tasks on other CPU cores
- Interference tasks on GPU copy engine

Interference tasks are memory intensive

Global Memory results



Execution time of the Frenet Path Planner Algorithm with CPU/GPU interference (Jetson Xavier)

GPU contention

GPU Contexts (Xavier)

Run 10527... | Run 10

Context switch in GPU execution. One task at time is executed (unless you use streams) (Jetson Xavier)

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- Context switch effect

- CPU processes interference
- Other kernels interference
- Other kernels interference

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- Other kernels interference

- Concurrent execution of CPU processes

- Concurrent execution of GPU processes

- Context switch effect
- CPU processes interference
- Other kernels interference
- Other kernels interference

- Concurrent execution of CPU processes
- Concurrent execution of GPU processes (spawned by CPU processes)

- Context switch effect
- CPU processes interference
- Other kernels interference
- Other kernels interference

- Concurrent execution of GPU processes (spawned by a single CPU process using streams)

- Context switch effect
- CPU processes interference
- Other kernels interference
- Other kernels interference (SMs contentions)

We can manage the GPU execution using streams but some aspect are unpredictable. Including CPU processes interference.

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And it impact the execution time



Moreover the System state can be affected by the environmental state. And it affect itself the execution time.



- Environment State
- System state

Impact the execution time:

- Input of computation
- Resource contention

It is difficult to estimate the execution time!



Estimation of interference

- Estimate the execution time of a kernel based on:
- Input (Environment State)
- other running task (System state)

It needs a large set of experiments

- limit the set to a significant states
- couple the estimation with scheduler aware of execution time and possible interferences